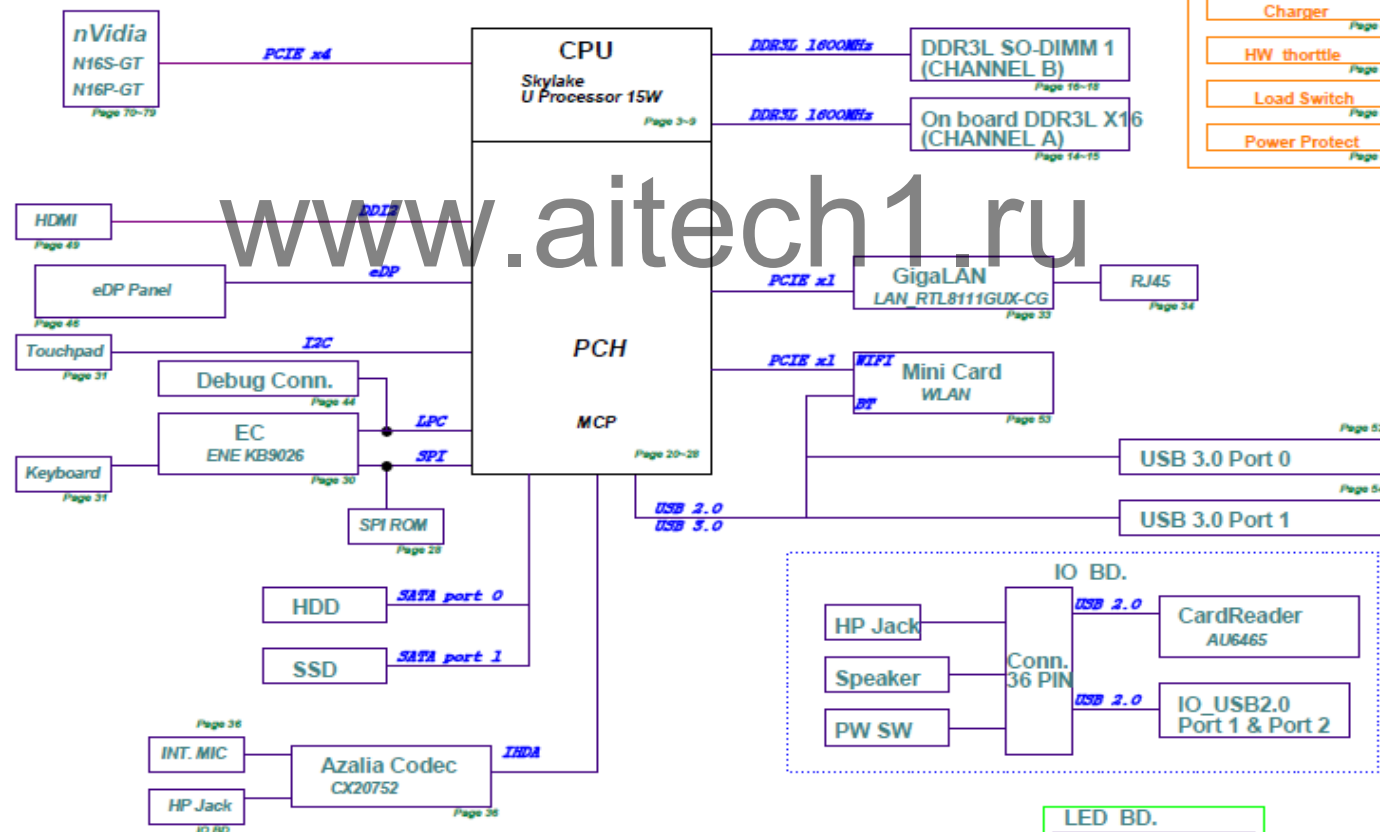


BLOCK DIAGRAM

K501 UB/UX SCHEMATIC
(UB : DGPU=Nvidia N16S-GT(GT940))
(UX : DGPU=Nvidia N16P-GT(GTX950))

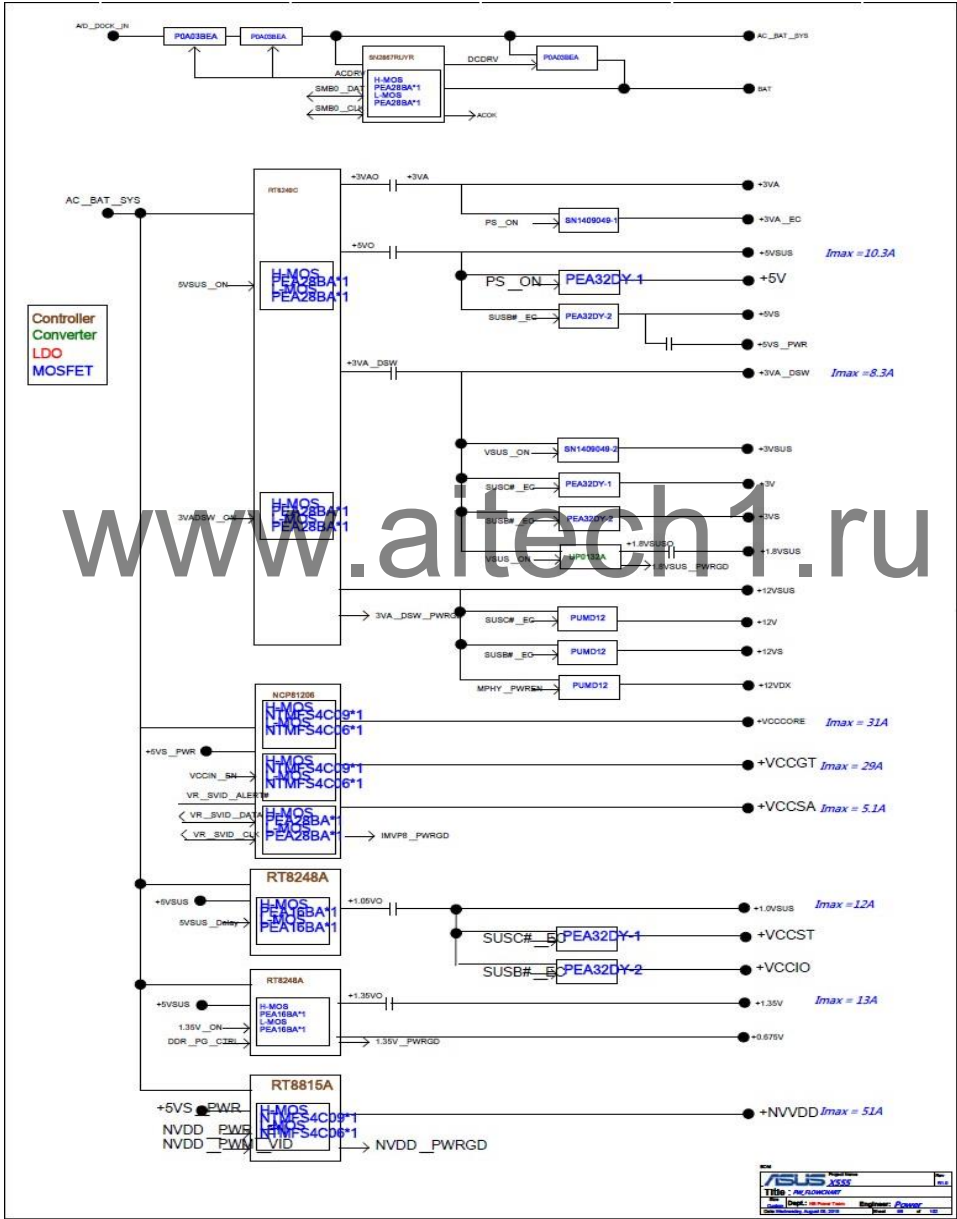


| | |
|----|-------------------------|
| 92 | POWER FLOWCHART |
| 95 | SYSTEM HISTORY |
| 96 | POWER-ON SEQUENCE |
| 97 | POWER-ON TIMING AC MODE |
| 98 | POWER-ON TIMING DC MODE |

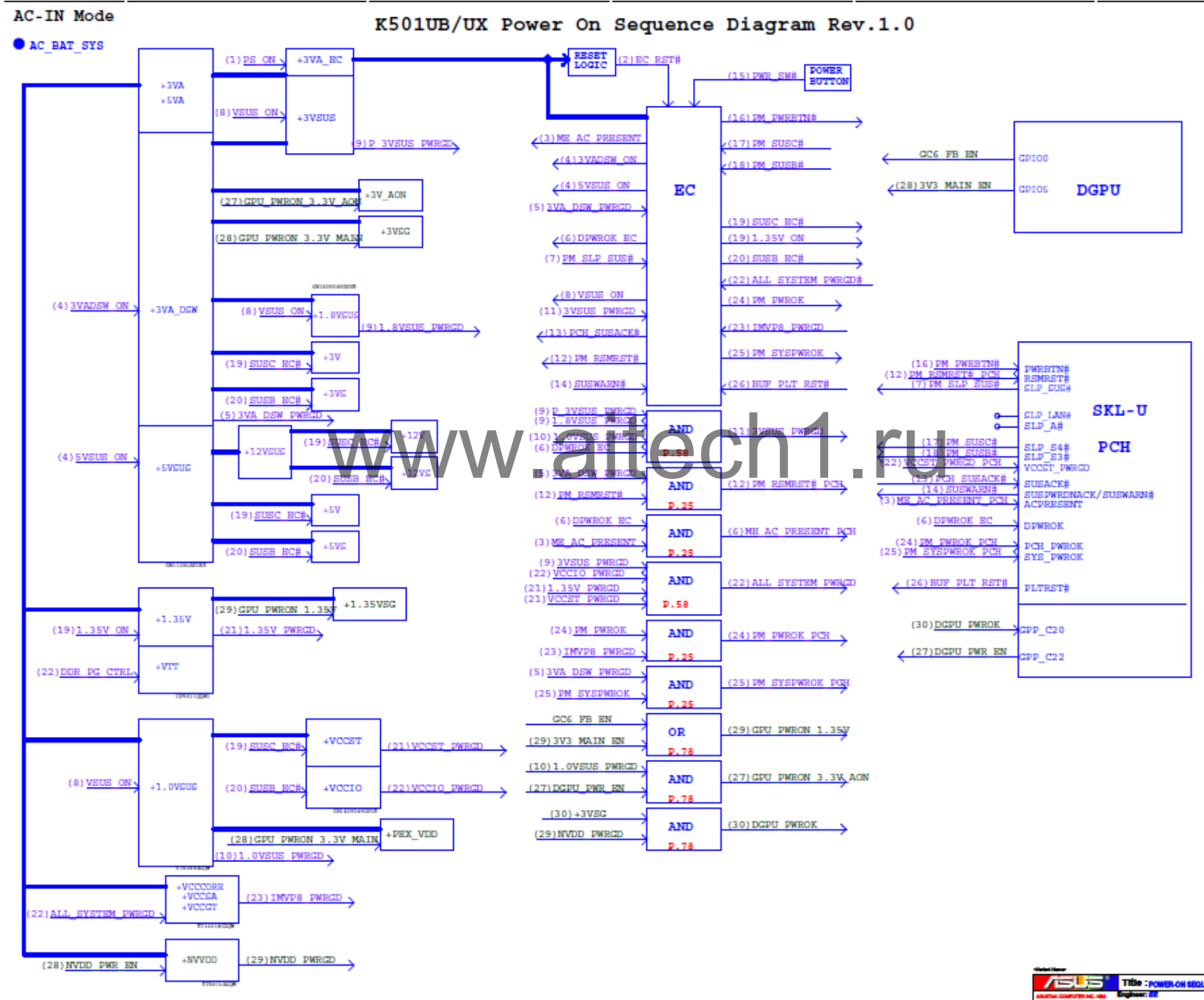
| | | |
|---|--|--------------------------------------|
| Discharge Circuit <small>Page 57</small> | DC & BATT. Conn. <small>Page 60</small> | PWM Fan <small>Page 50</small> |
| Reset Circuit <small>Page 52</small> | Screw Holes <small>Page 65</small> | CMOS Camera <small>Page 4</small> |

LED BD.
Conn. 12 PIN

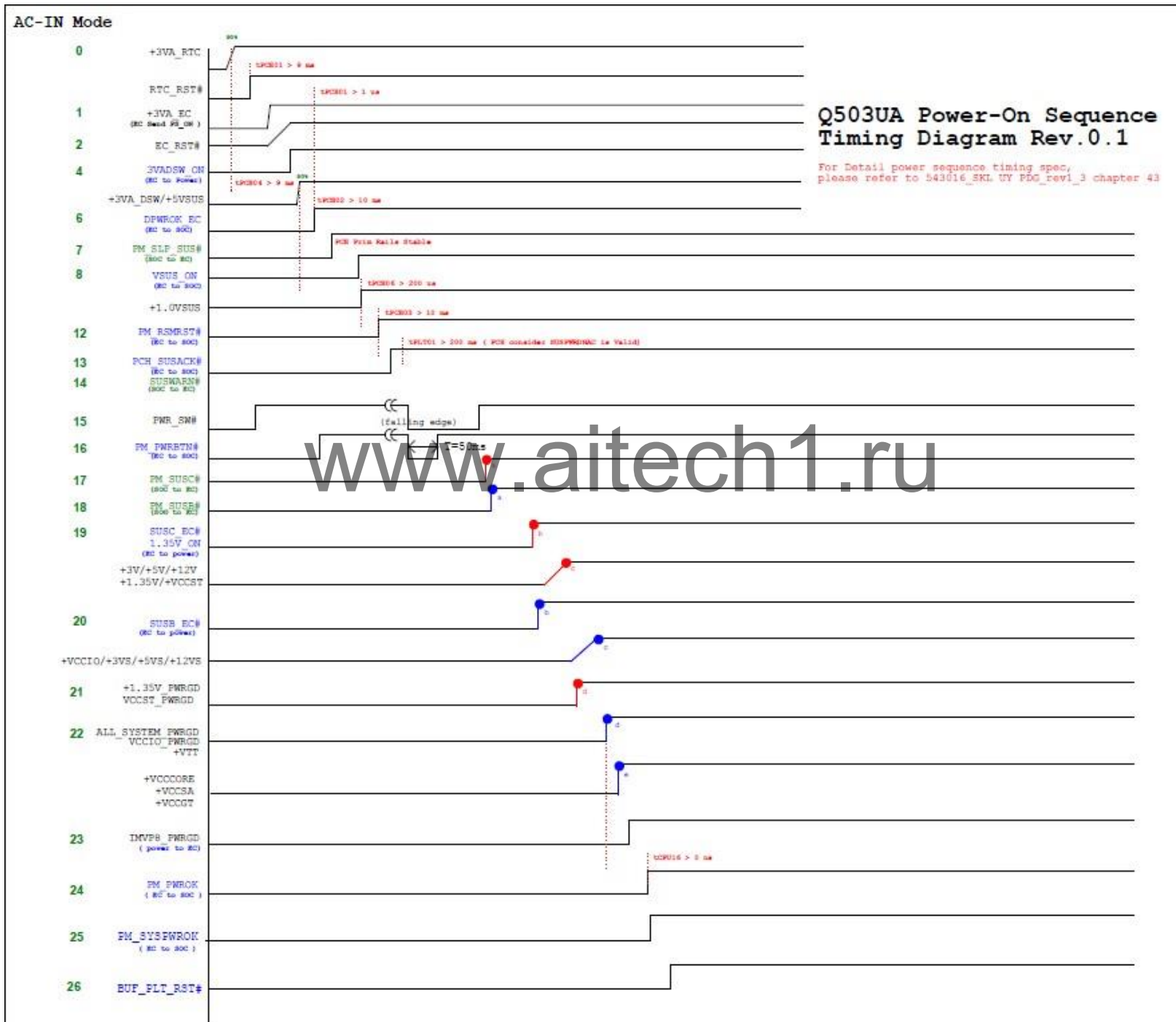
POWER FLOW



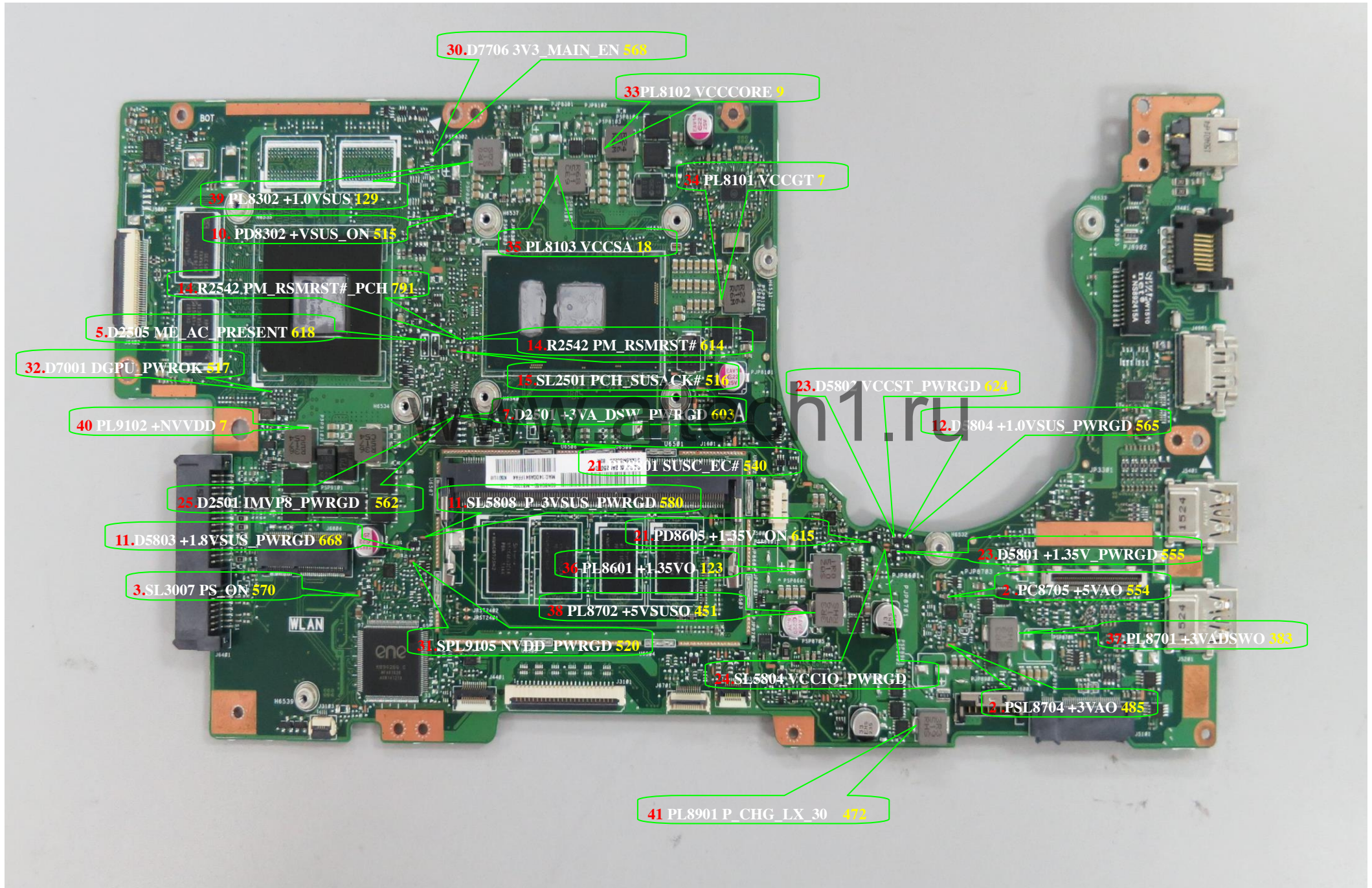
POWER ON SEQUENCE



AC POWER ON SEQUENCE



Signal Measure Point-Bottom



Signal Measure Point-Top

